

The logo for nLIGHT is displayed within a dark blue rectangular background. The letter 'n' is in a white, italicized serif font, while the letters 'LIGHT' are in a white, bold, sans-serif font.

n LIGHT

**> 73% CW Wall-Plug Efficiency at High Powers from
0.94- μm and 0.98- μm Emitting Laser Diodes**

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** Our work to improve 9xx-nm device efficiency is supported by DARPA under the SHEDs program contract number: MDA972-03-C-101. This material is approved for public release, distribution unlimited*

nLight Overview



- **History**
 - Founded in 2000
 - Over 100 employees
- **Technology**
 - High power laser diodes from 630 to 1900 nm
 - Broad range of packages
- **Production**
 - 60,000 sq ft vertically integrated manufacturing facility
 - Complete capabilities with MOCVD through packaging

nLight's products range from several Watts to several tens of kW

Product category	nLight product examples
Single Emitter <5W	
Diode Arrays 40 to 100 Watts	
Stacks of Arrays >100W	
Fiber Bundled Arrays < 40 Watts	

nLIGHT

nLight laser diode technology milestones

Capability

Key demonstrations

6xx nm

• 60 Watts CW

8xx nm

• 364 Watts CW

9xx nm

• Over 75% efficiency

14xx nm

• 88 Watts CW

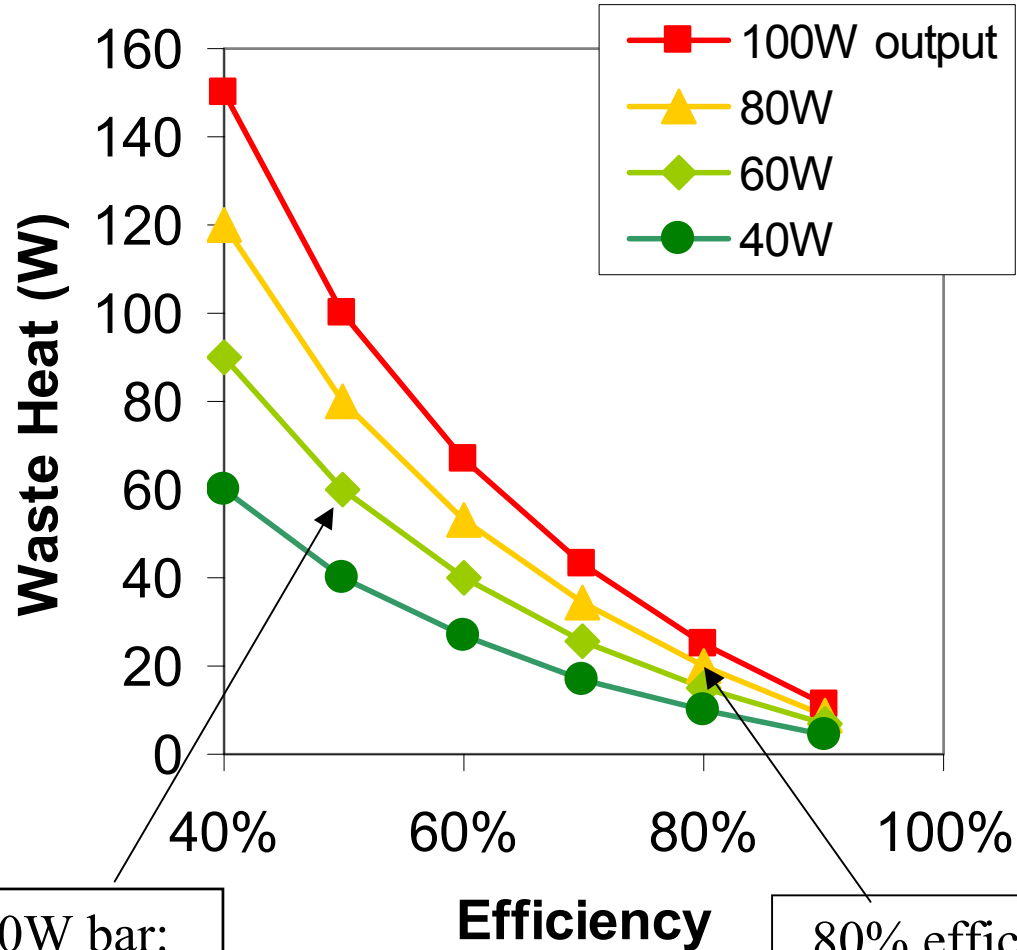
Packaging

• High reliability at 100 Watt CW product

- **Benefit of Increased Efficiency**
- **Summary of Progress over 18 Months**
- **Experimental Approach**
- **Key laser parameters and progress in improving them**
- **Insights on limiting physics from cryogenic testing**
- **Robustness: reliability, COMD test**
- **Conclusions**

Simpler Thermal Management with Increased Efficiency

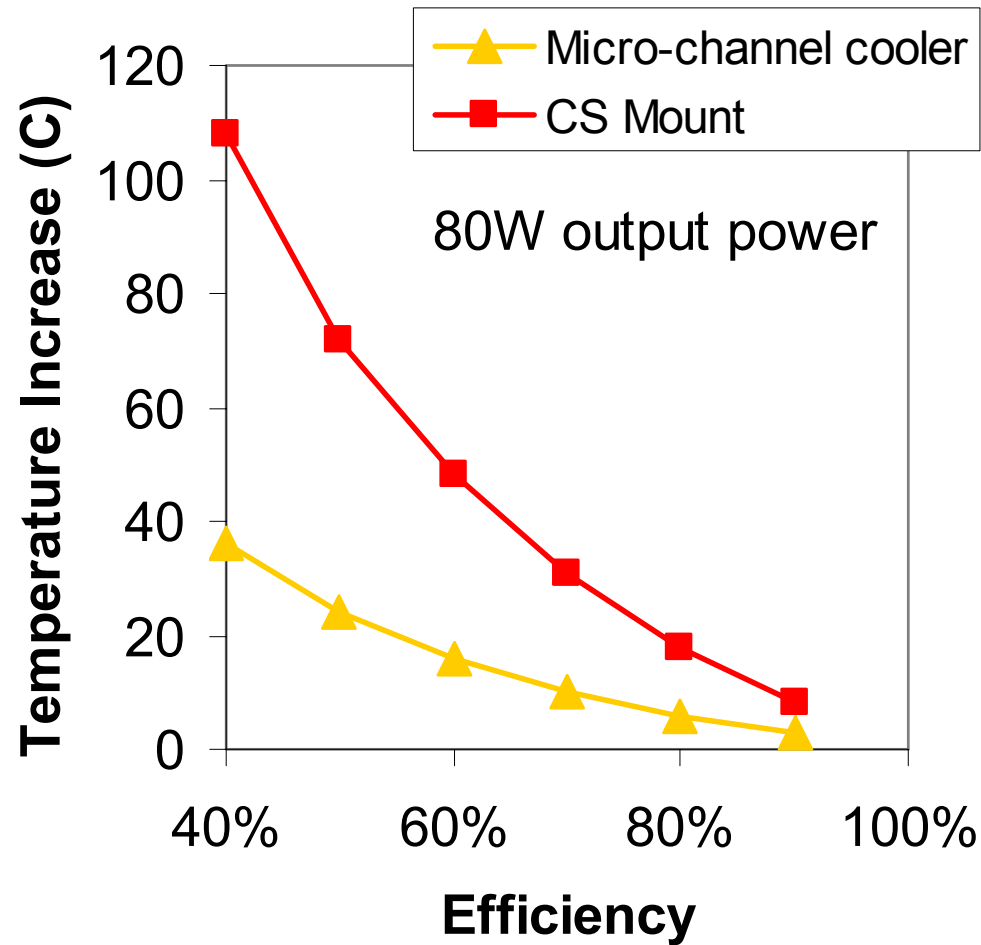
> 3x Reduction in Heat Dumped into Cooling System



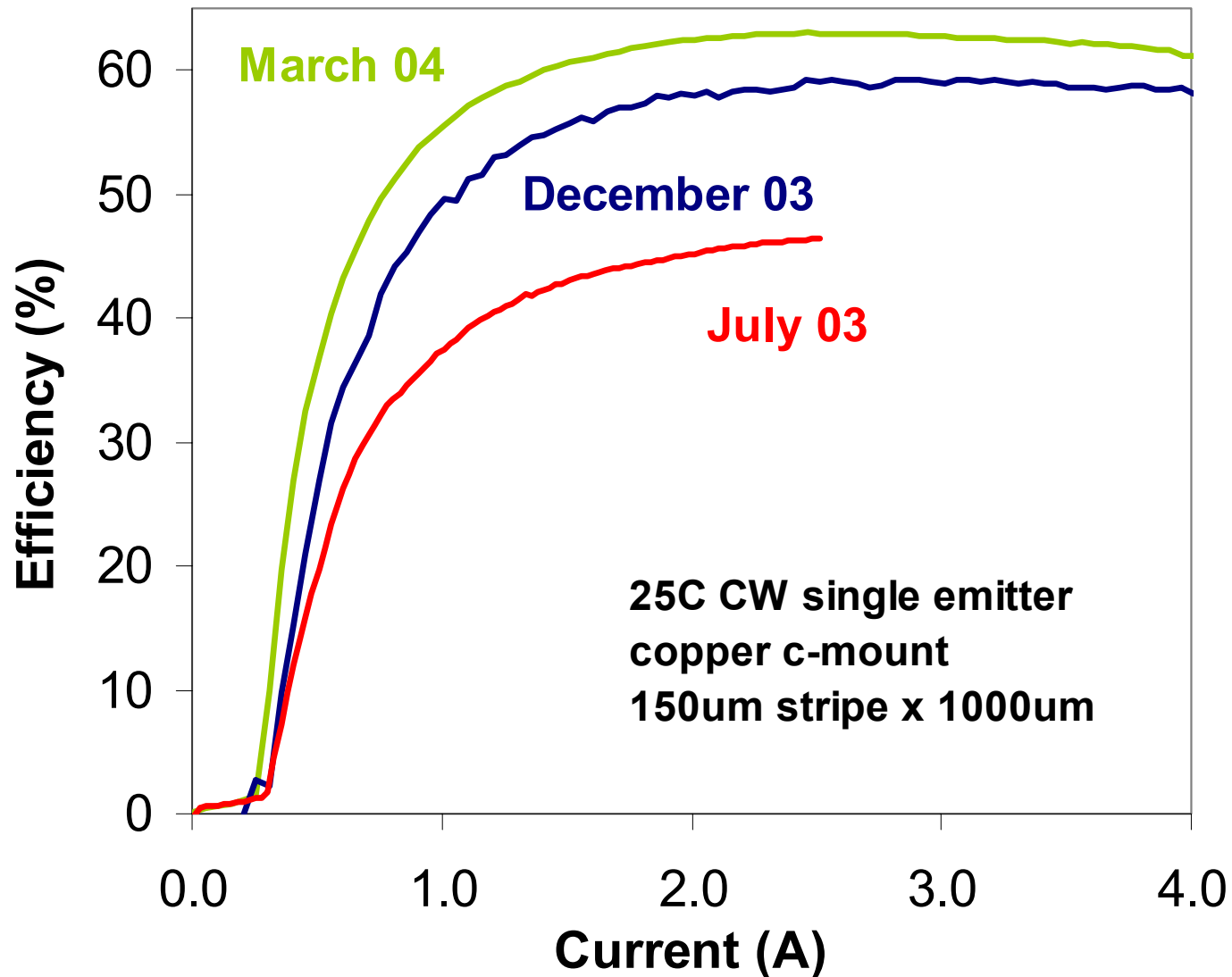
50% efficient 60W bar:
60W heat dumped per bar

80% efficient 80W bar:
20W heat dumped per bar

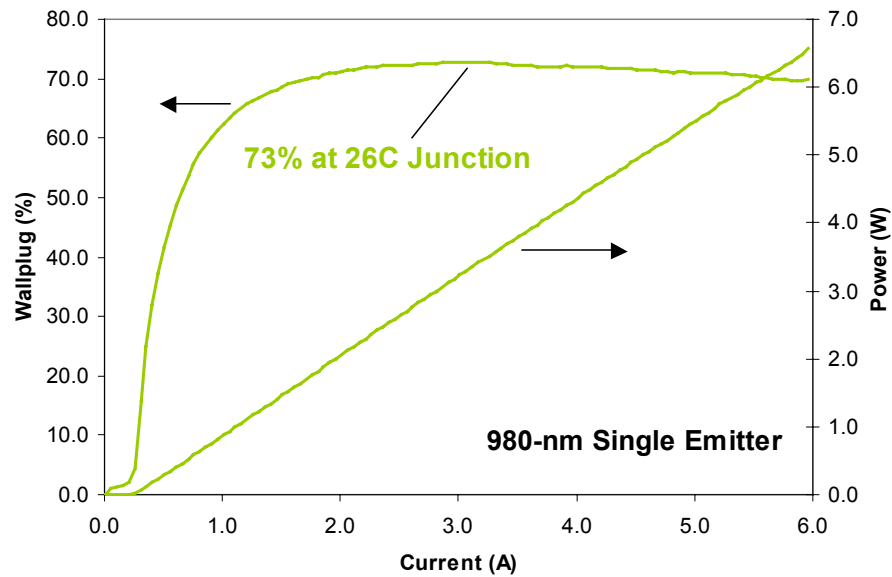
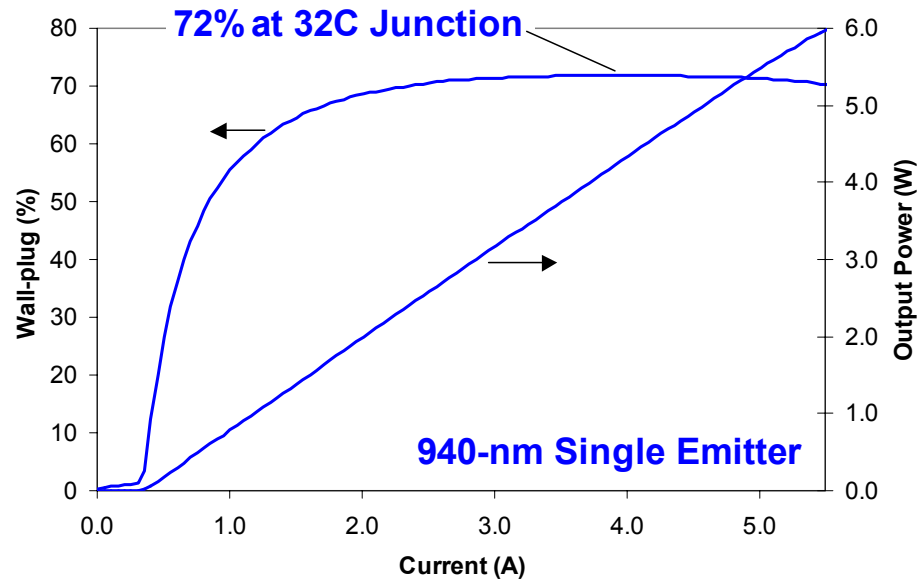
Bars Operate Cooler with Increased Efficiency



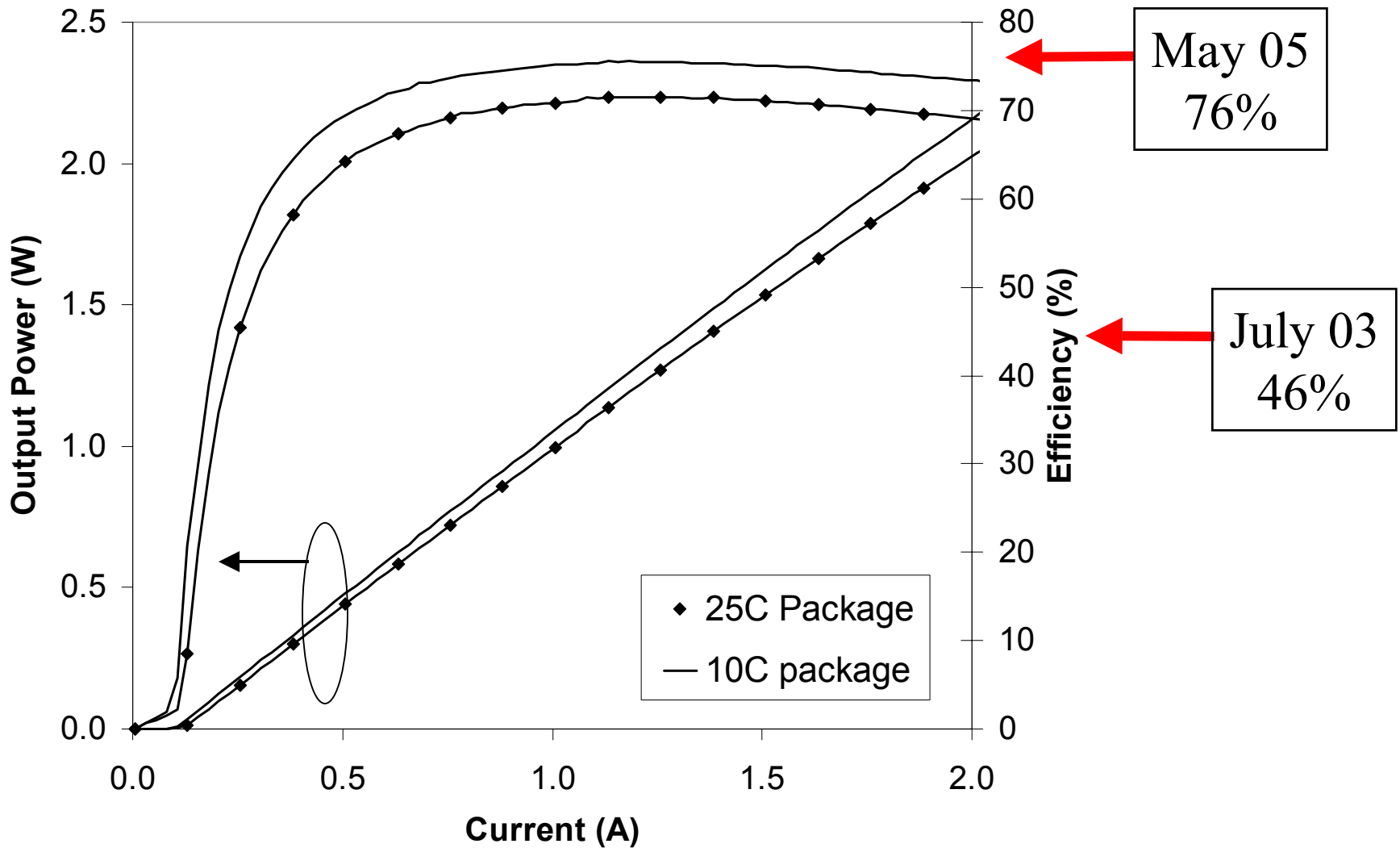
Improve efficiency of bar from 50%-80%
Reduce CW temperature of an 80W bar by > 20C



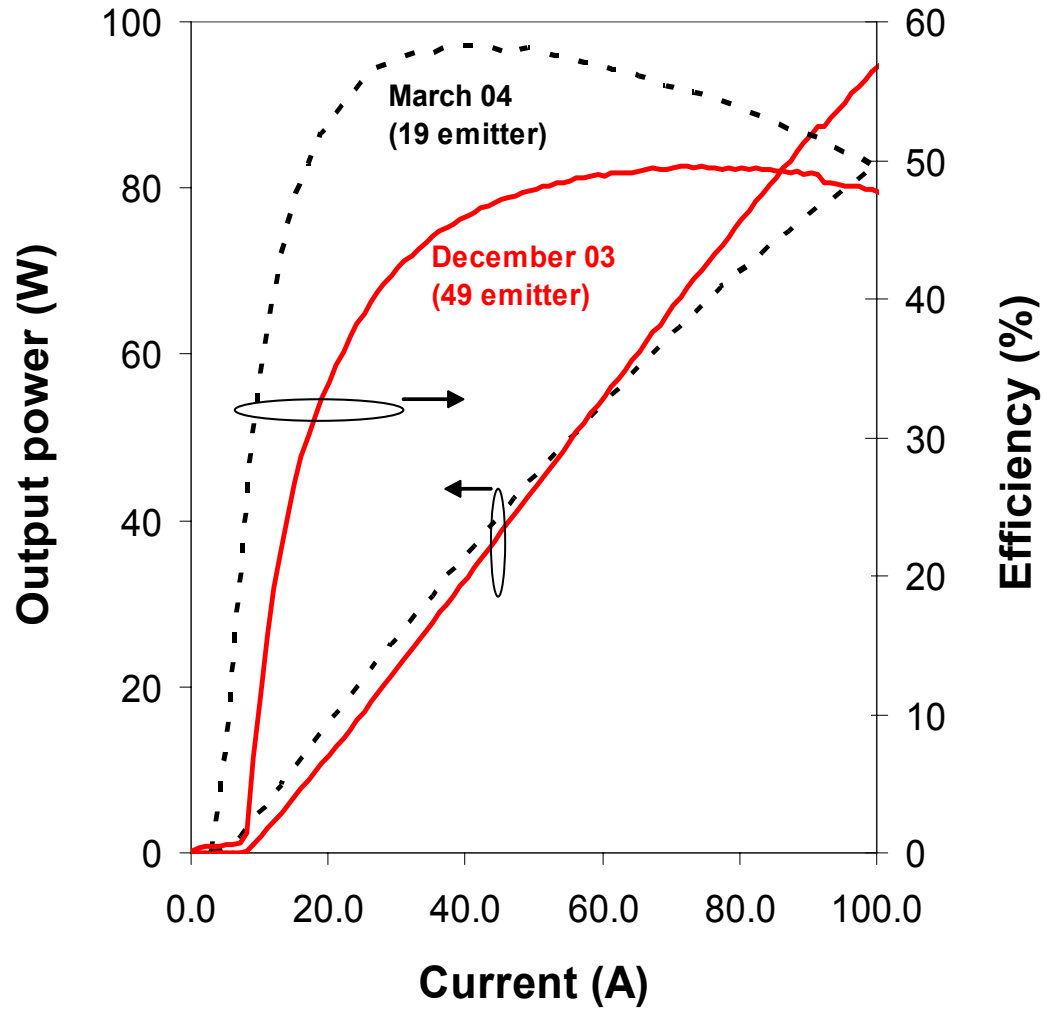
Stable, Wavelength Independent 73% Achieved



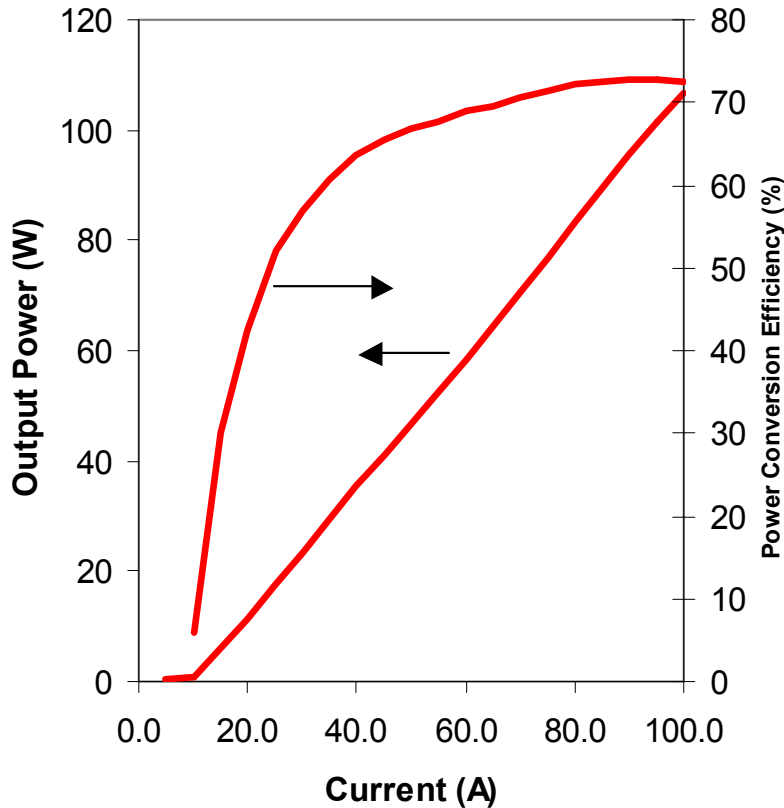
Latest Peak Performance Design: 76% Wall-plug



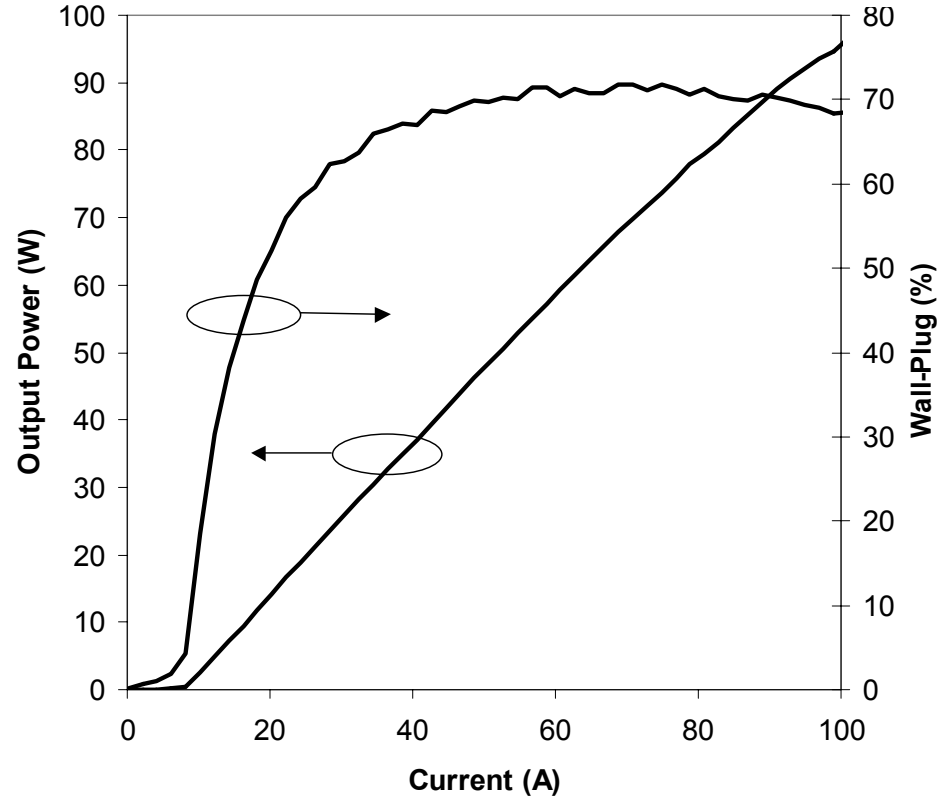
Bars: Initial Rapid Improvement



High Efficiency Bars: 73% 100W QCW, 72% 70W CW

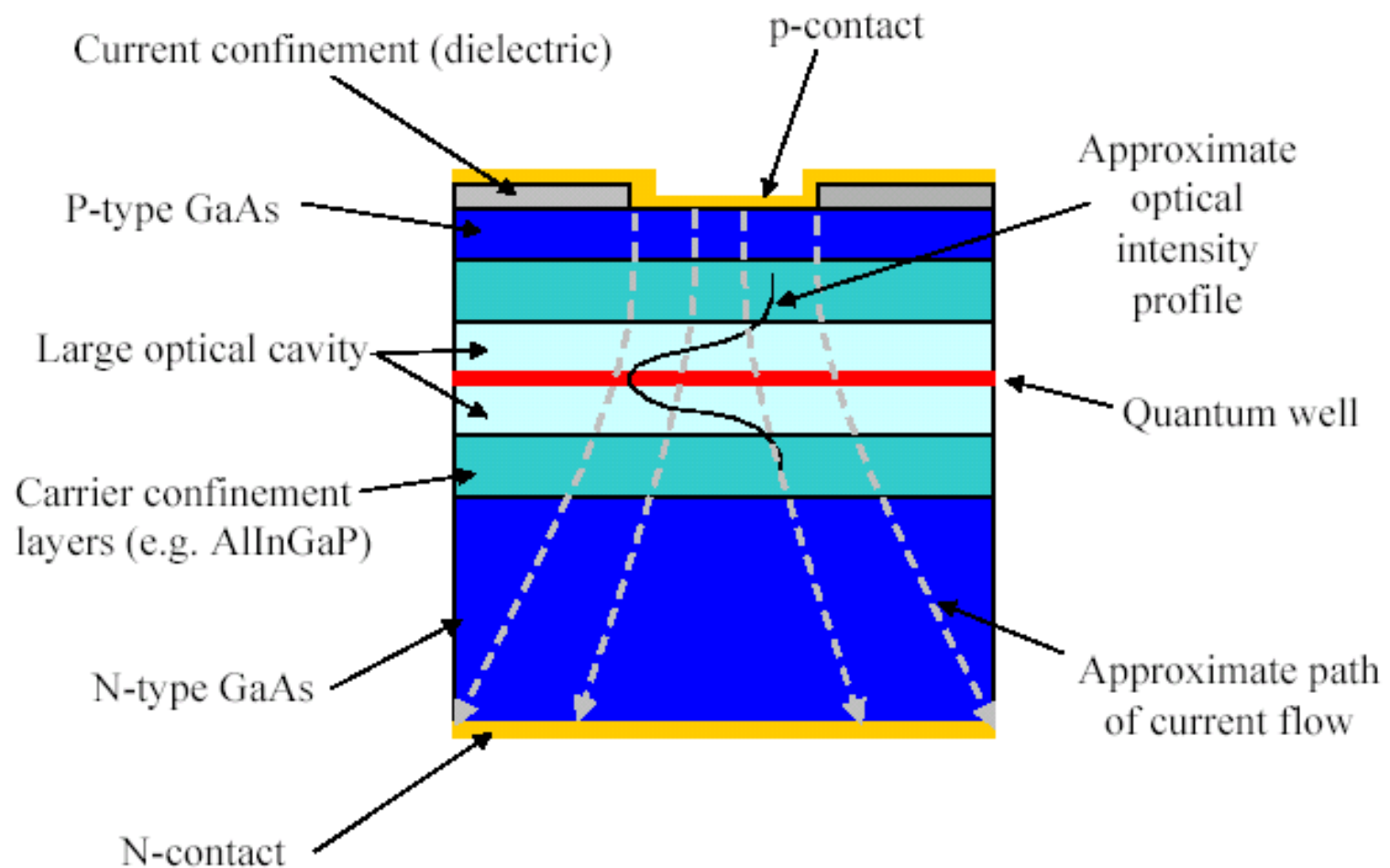


50% fill factor 1mm cavity 25C
QCW test (200 μ s, 100Hz)



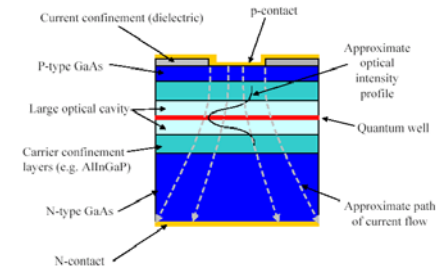
50% fill factor 1mm cavity
35C Junction CW test

Laser Cross-Section



Overall Design Approach

- **Break down all contributors to laser efficiency**
 - Characterize, model, optimize
- **Optimize materials and interfaces by experiment**
 - Contact / interface resistance
 - Bulk mobility
 - Low temperature photoluminescence
- **Systematic approach**
 - Rigorous physics-based modeling
 - Detailed root cause materials analysis
- **Use high performance facet passivation**
 - Open up design space



3 key Terms Limit Power Conversion Efficiency

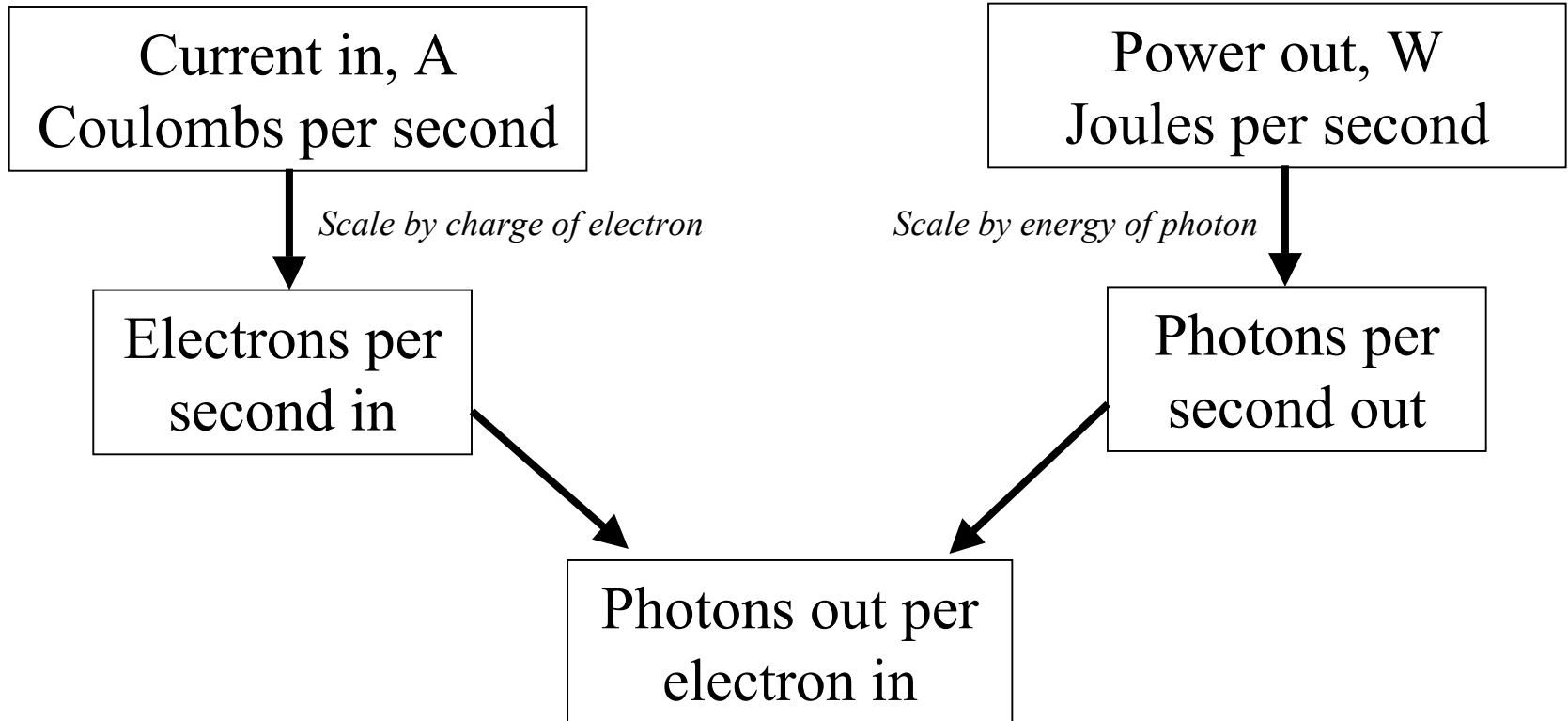
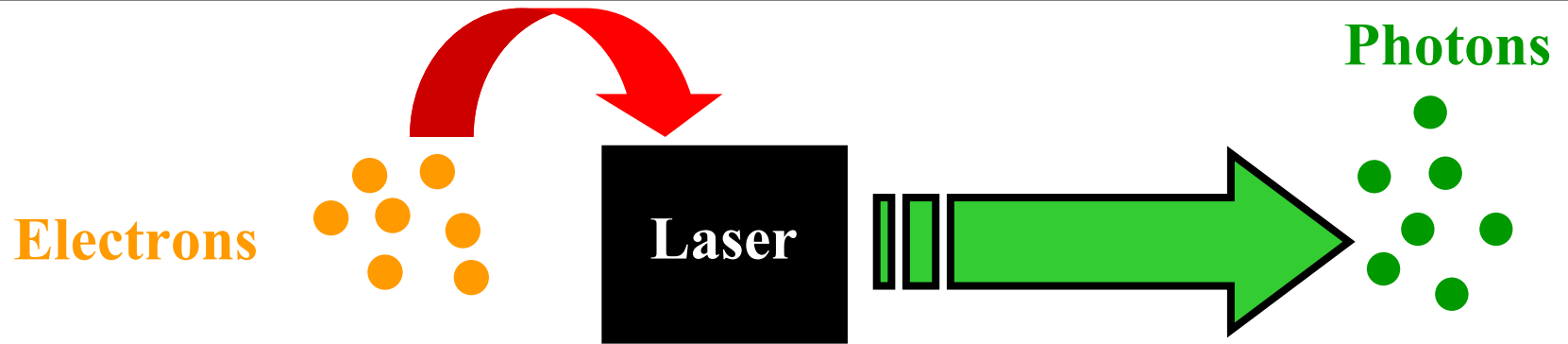
$$PCE = \frac{E_{ph} \times \eta_{ext}^{(d)} (I - I_{th})}{I \times (V_{BG} + V_D)}$$

	Parameter	Definition
I_{th}	Threshold	Current for optical gain to overcome loss
$\eta_{ext}^{(d)}$	Differential Quantum Efficiency	Proportion of injected electrons converted to useful photons
V_D	Voltage Defect	Voltage over and above the lasing energy

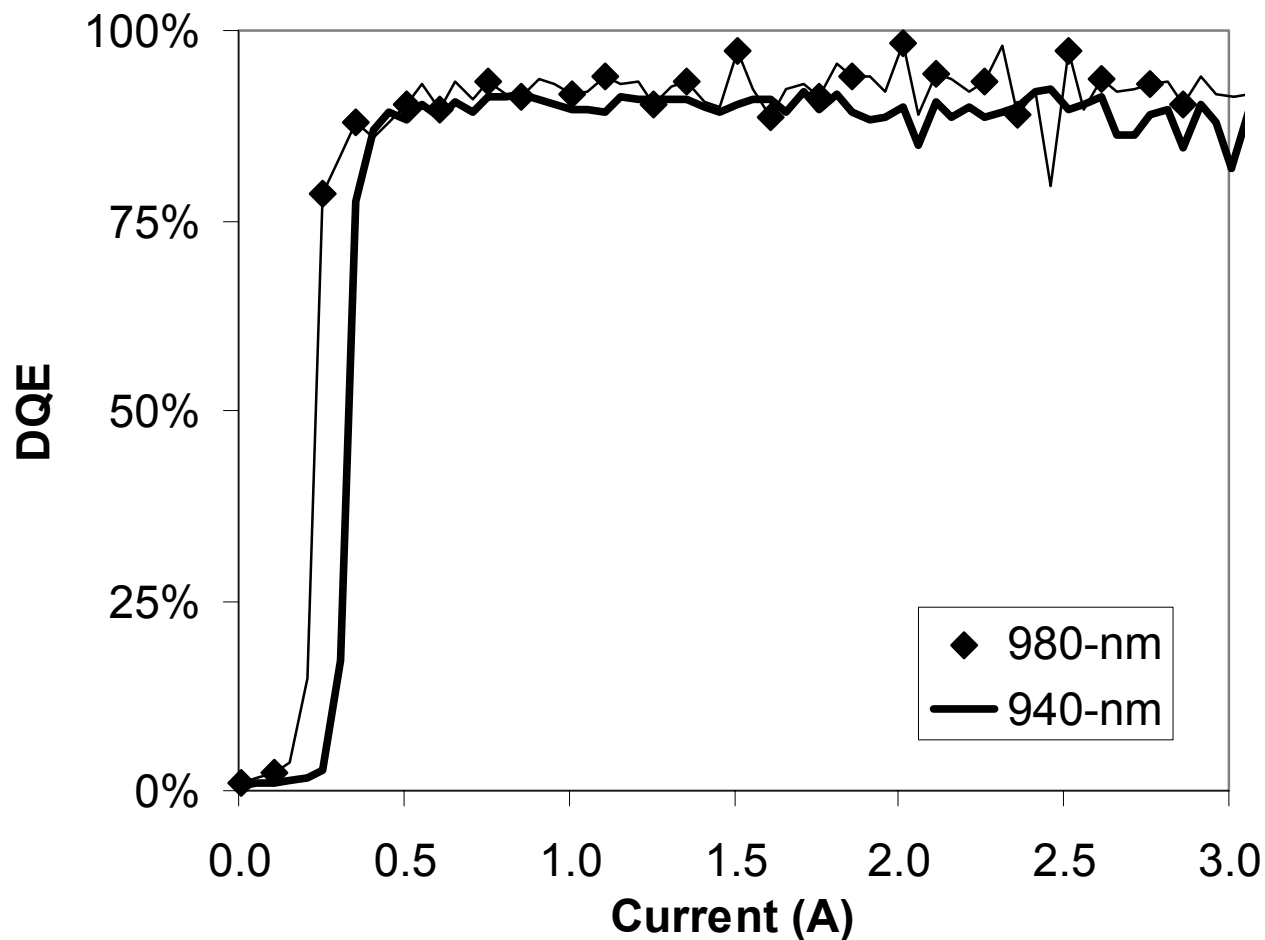
Improvement Approach Taken

Parameter	Approach
Threshold	Optimize Strain in quantum well
Slope	Minimize overlap of light with lossy regions
Voltage	Optimize hetero-junctions

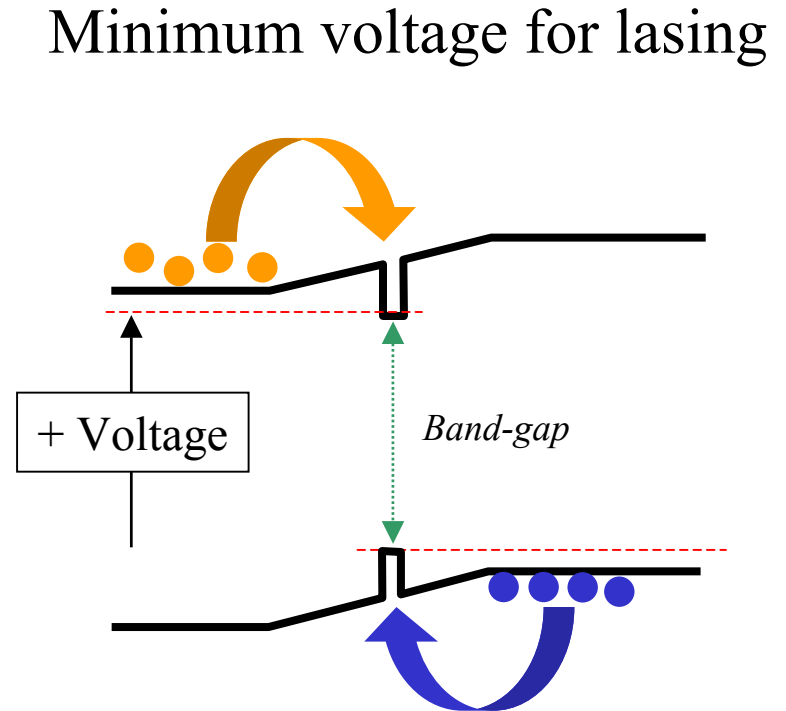
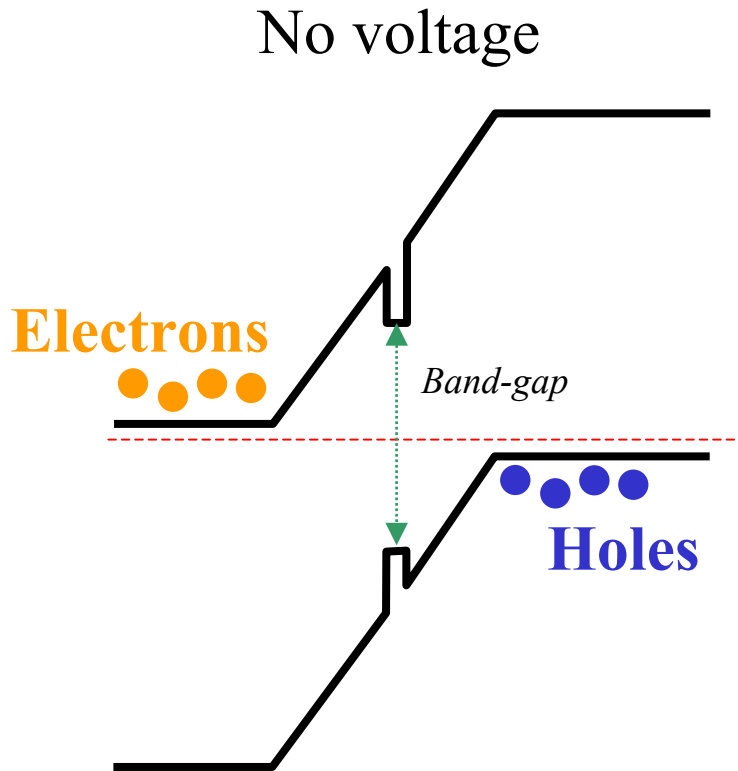
Key Term 1: Photons per Electron (DQE)



DQE ~ 90% Across Wavelength

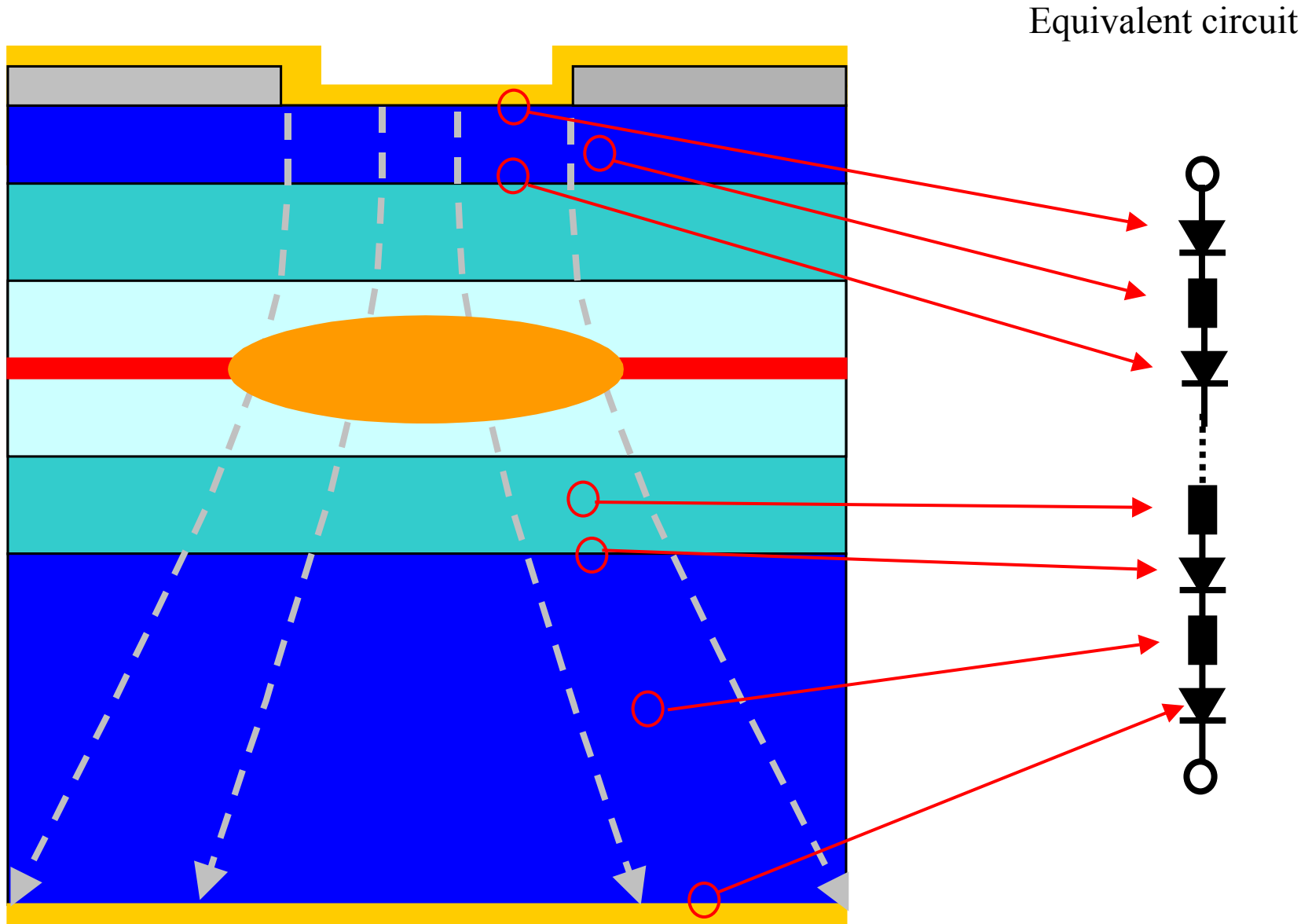


Key Term 2: Voltage Defect

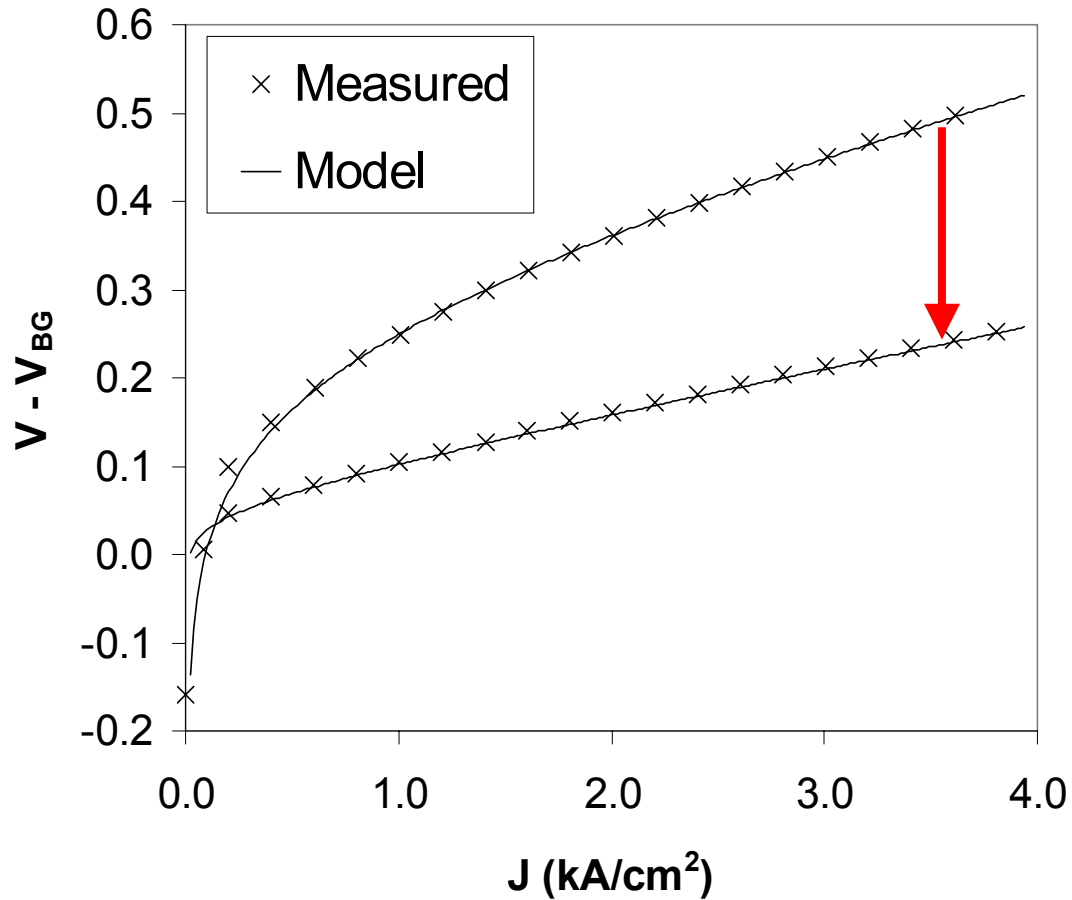


Minimum voltage is band-gap of quantum well
Any more is called the “voltage defect”

Every Laser Interface and Bulk Layer Adds Voltage



Minimize Junction Voltages



$$V = V_{BG} + \sum_i \sigma_i J + \sum_j V_j \ln \left(\frac{J}{J_j} \right)$$

Resistive Term \rightarrow $\sum_i \sigma_i J$ Diode Term \rightarrow $\sum_j V_j \ln \left(\frac{J}{J_j} \right)$

> 80% Drop in Diode Term

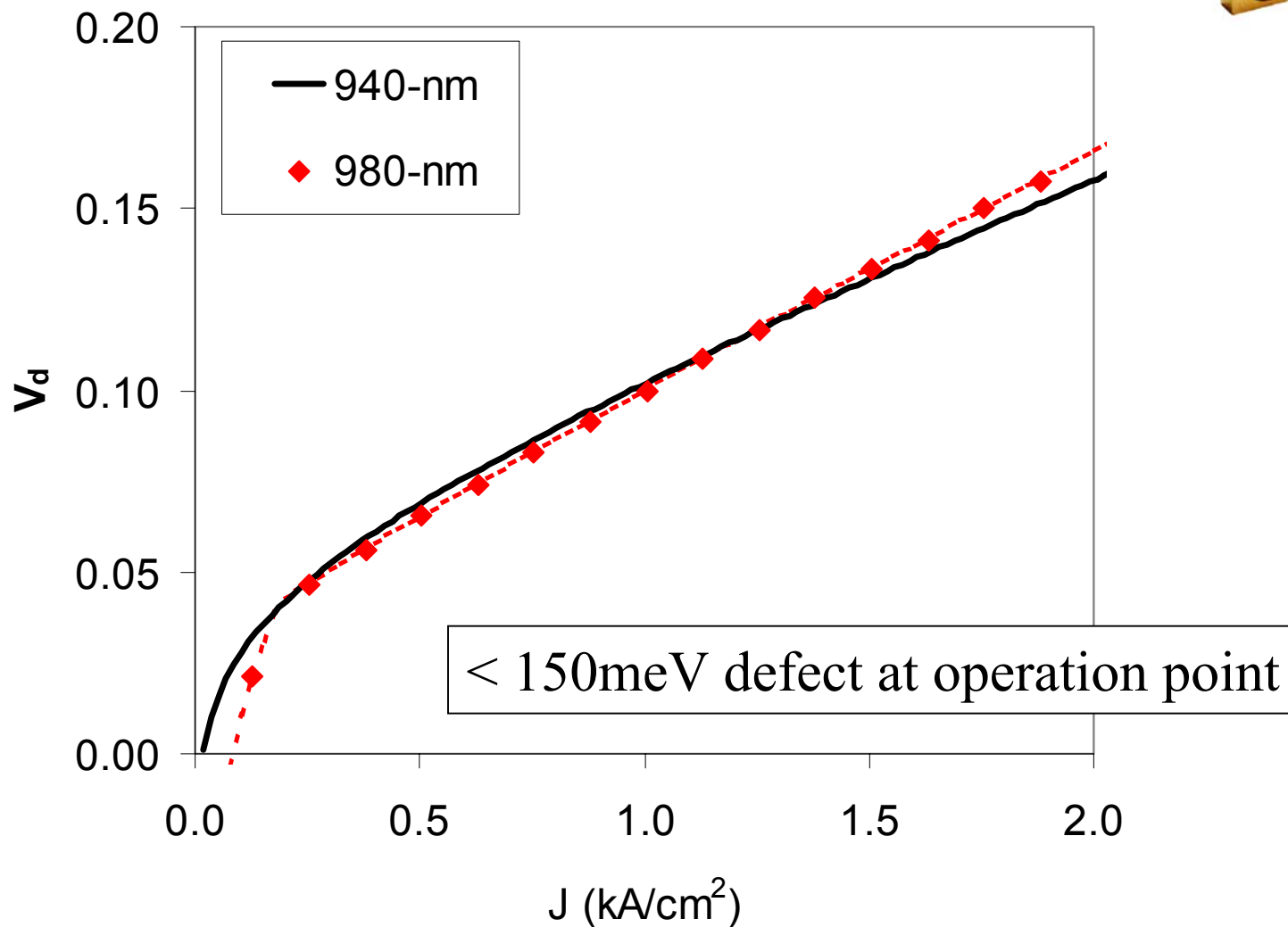
$$V = V_{BG} + \sum_i \sigma_i J + \sum_j V_j \ln\left(\frac{J}{J_j}\right)$$

Resistive Term \rightarrow $\sum_i \sigma_i J$ Diode Term \rightarrow $\sum_j V_j \ln\left(\frac{J}{J_j}\right)$

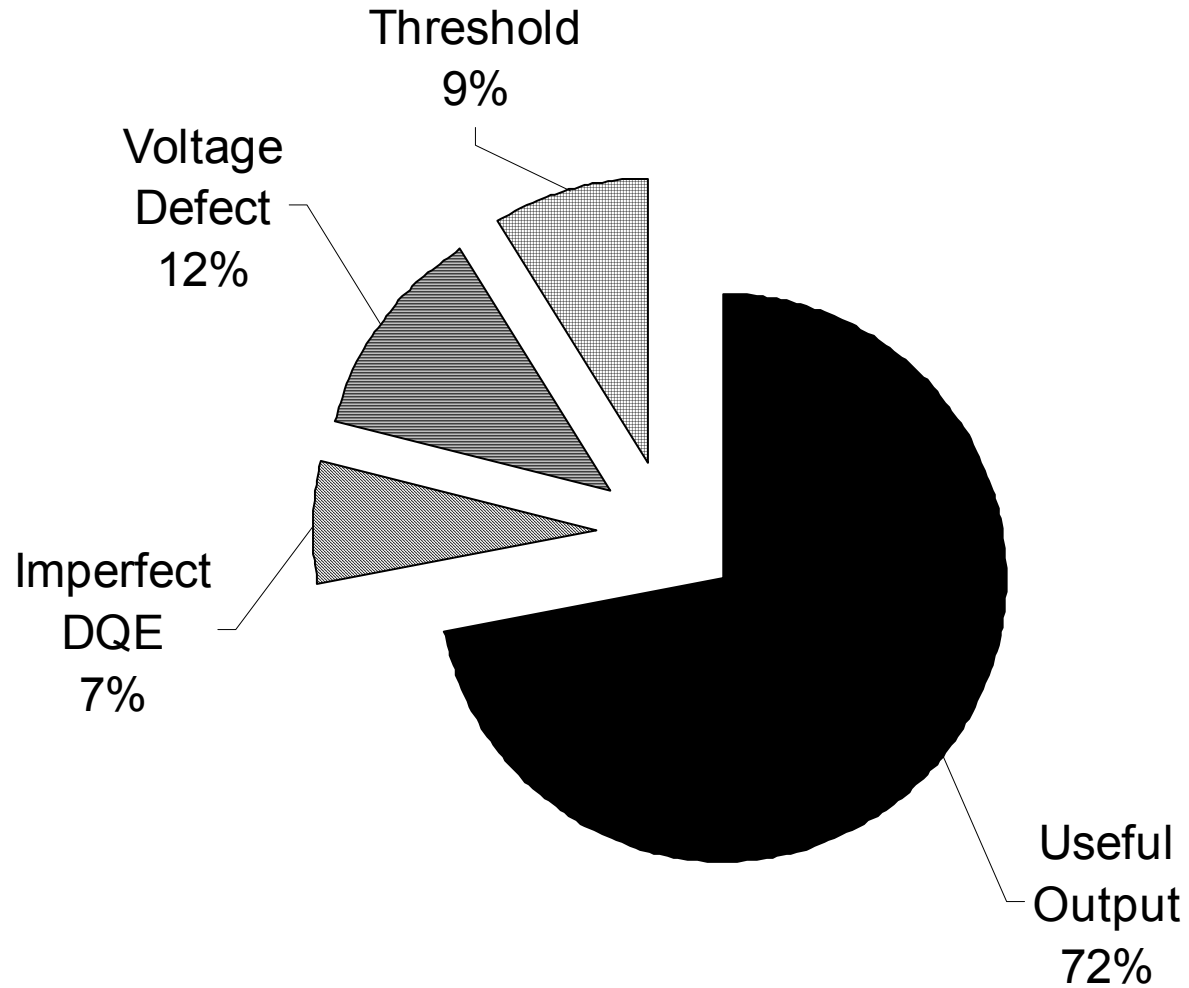
Design	$\sum_j V_j$ (meV)	$\sum_i \sigma_i$ (m Ω)	$\langle J_o \rangle$ (A/cm ²)
High Voltage	85.5	34.9	100
Low Voltage	14.2	30.8	20
	-83%	-12%	-80%

Systematic experimental study

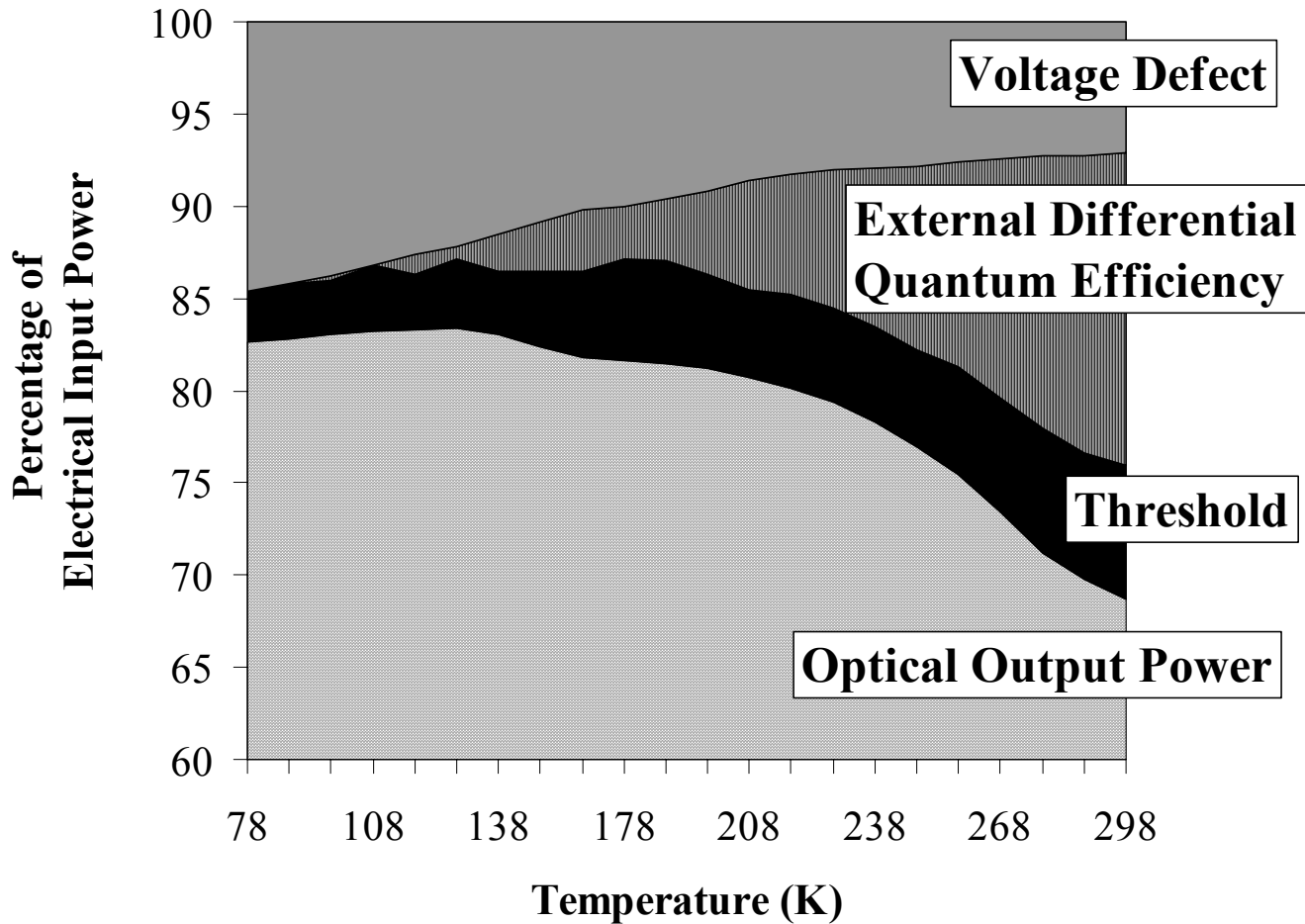
Eliminates junction voltages



Overall Efficiency Break-Down



Key Terms over Temperature

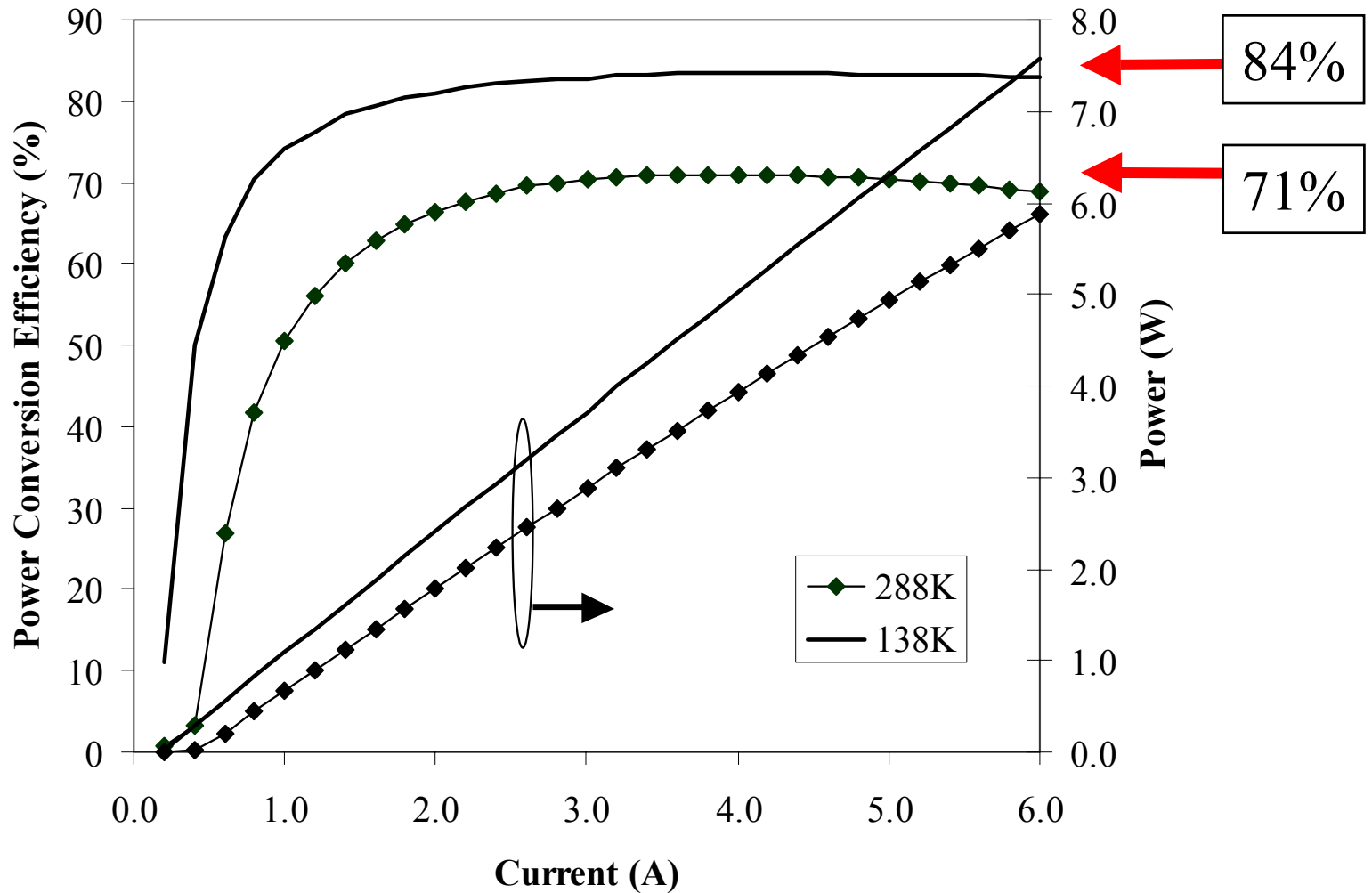


Cryogenic measurements kindly performed by:

L. S. Meng and J. K. Brasseur

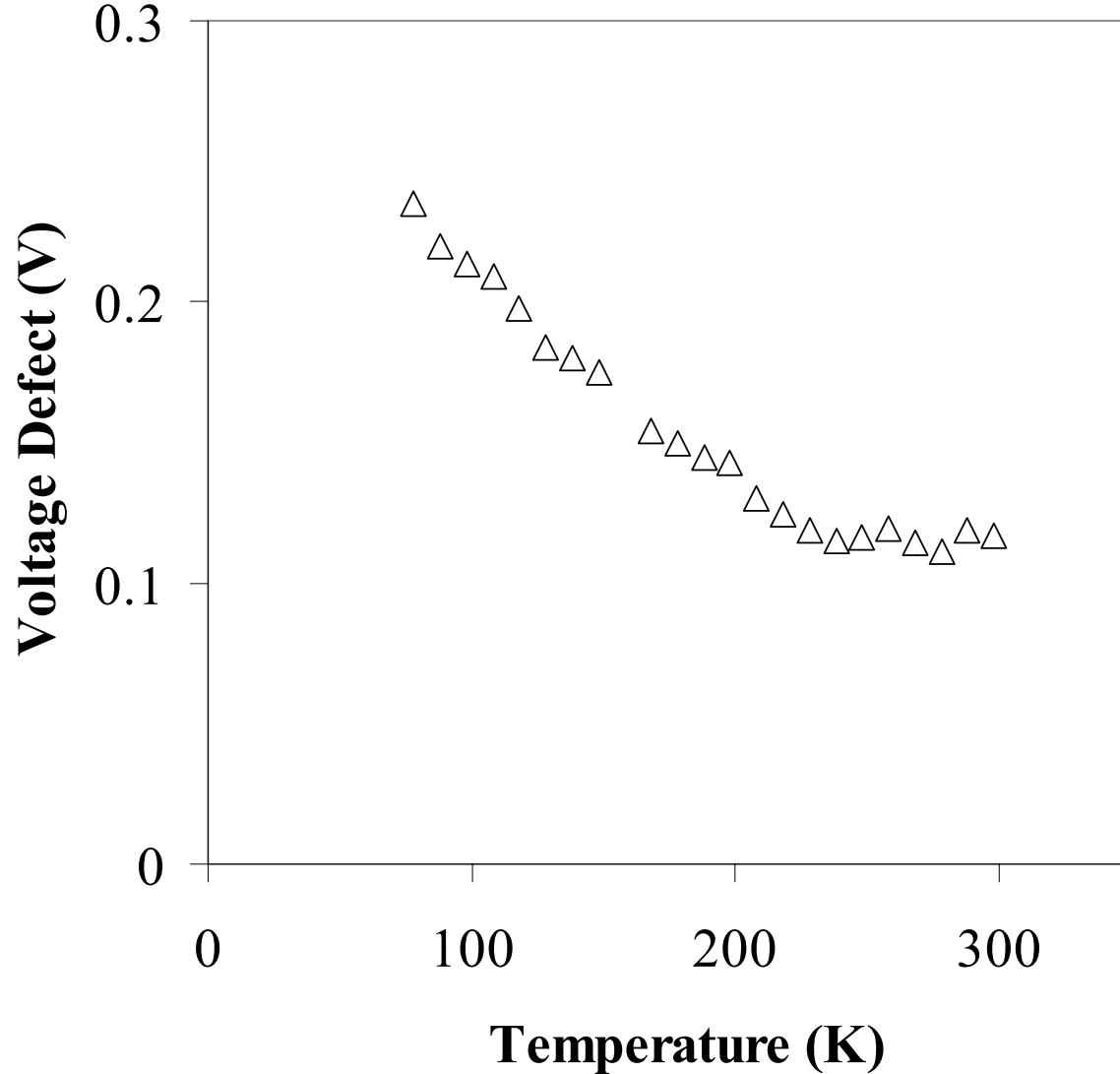
(Directed Energy Solutions, Colorado Springs, United States of America)

Peaks at 84% Efficiency



Cryogenic measurements kindly performed by:
L. S. Meng and J. K. Brasseur
(Directed Energy Solutions, Colorado Springs, United States of America)

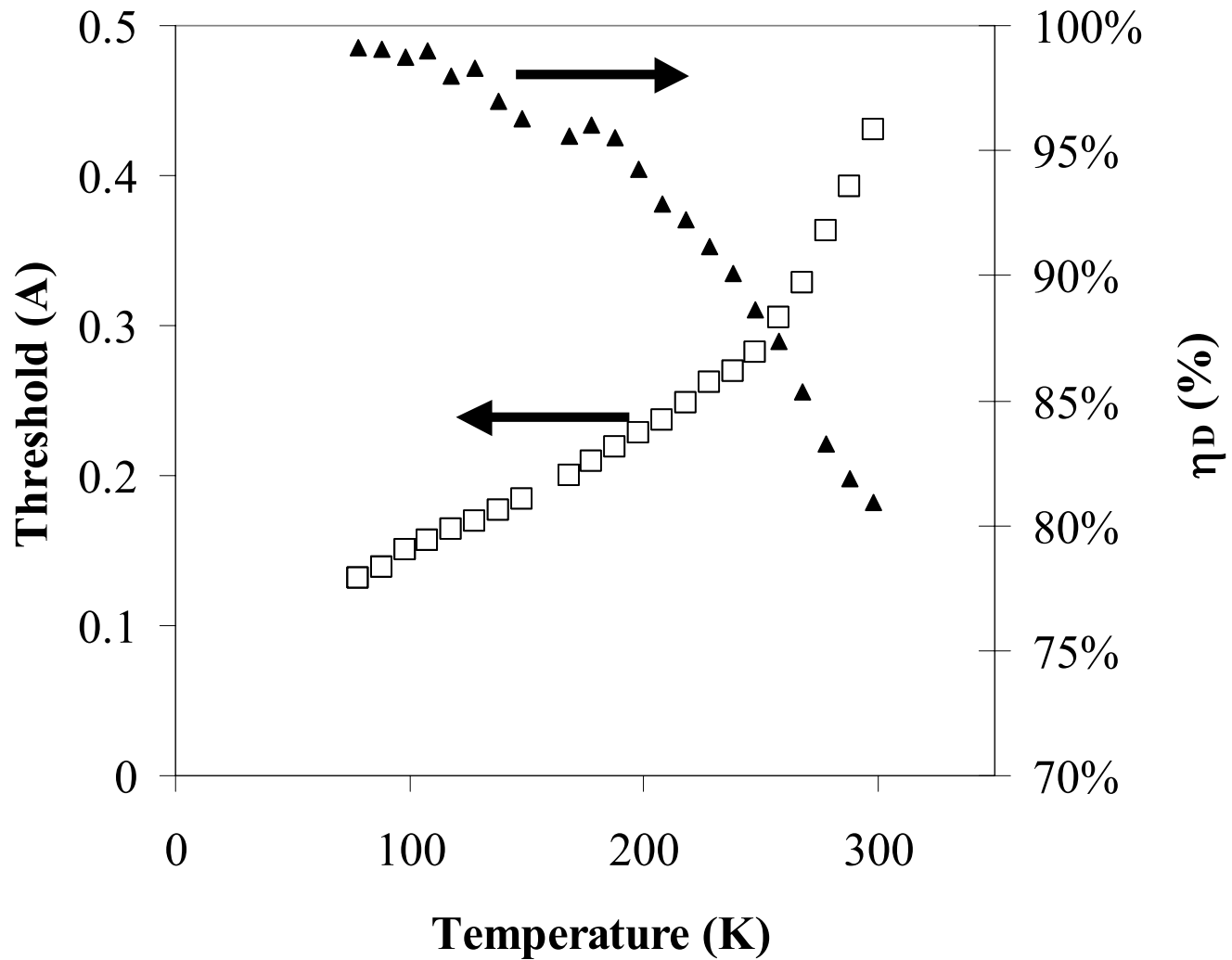
Voltage Defect Increases at Low Temperatures



Cryogenic measurements kindly performed by:

L. S. Meng and J. K. Brasseur

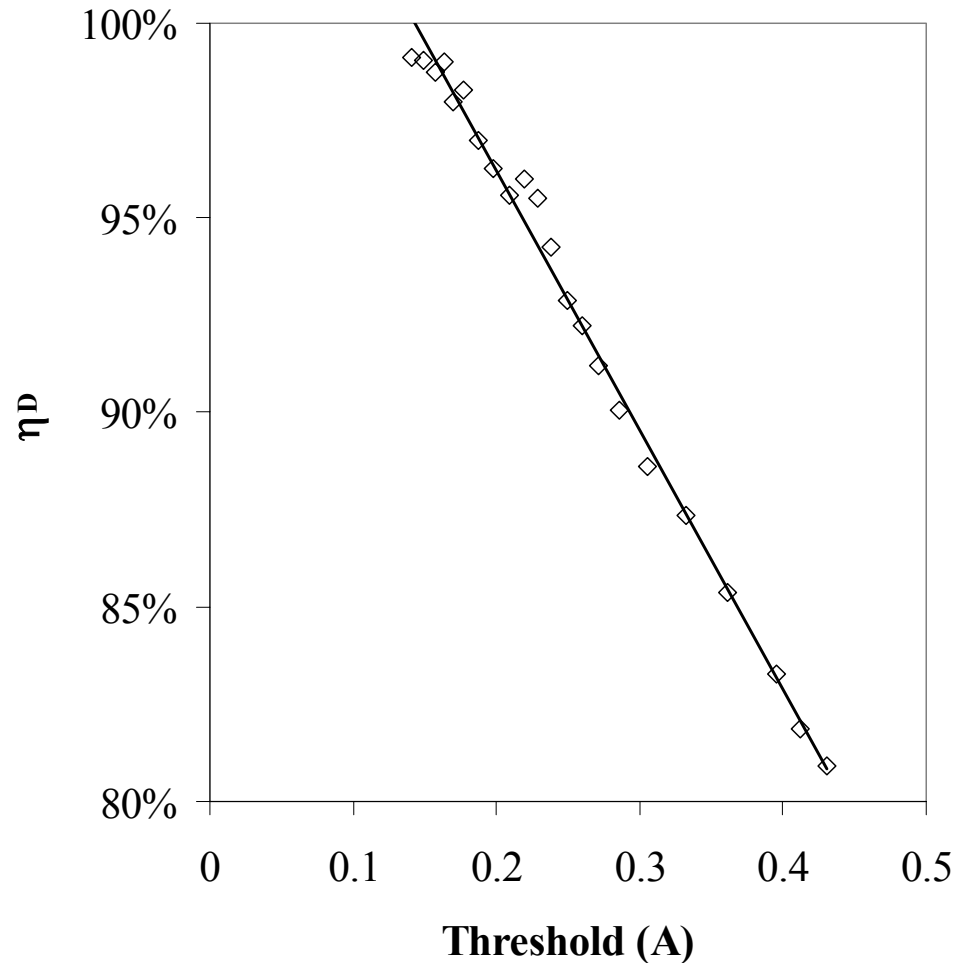
(Directed Energy Solutions, Colorado Springs, United States of America)



Cryogenic measurements kindly performed by:

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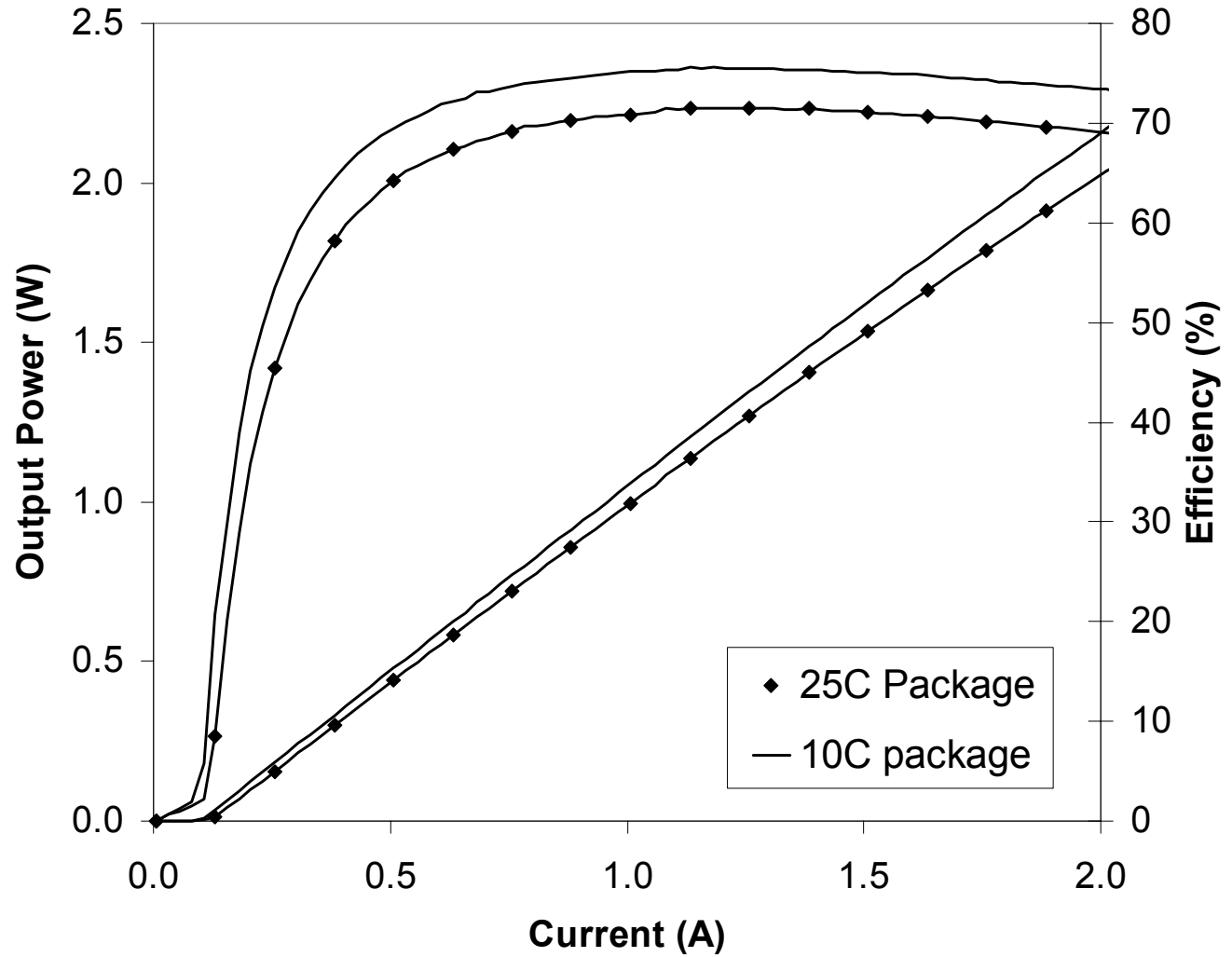
Indicates DQE strongly linked to effects in the well

Cryogenic measurements kindly performed by:

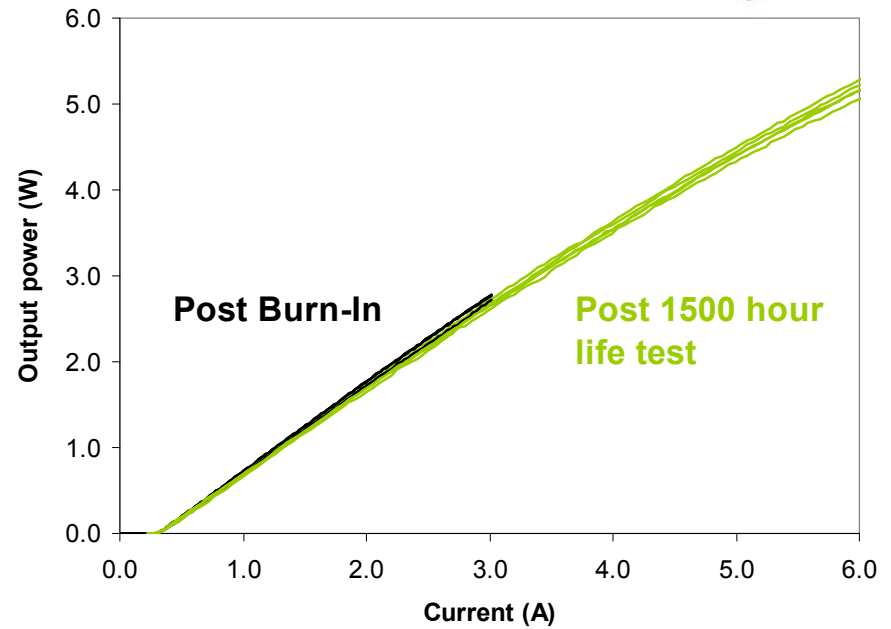
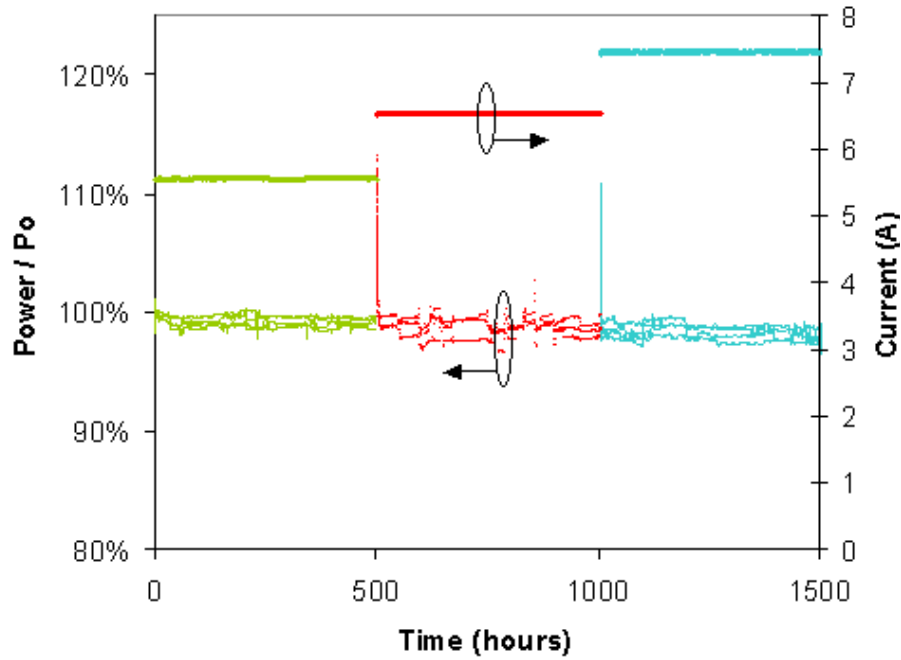
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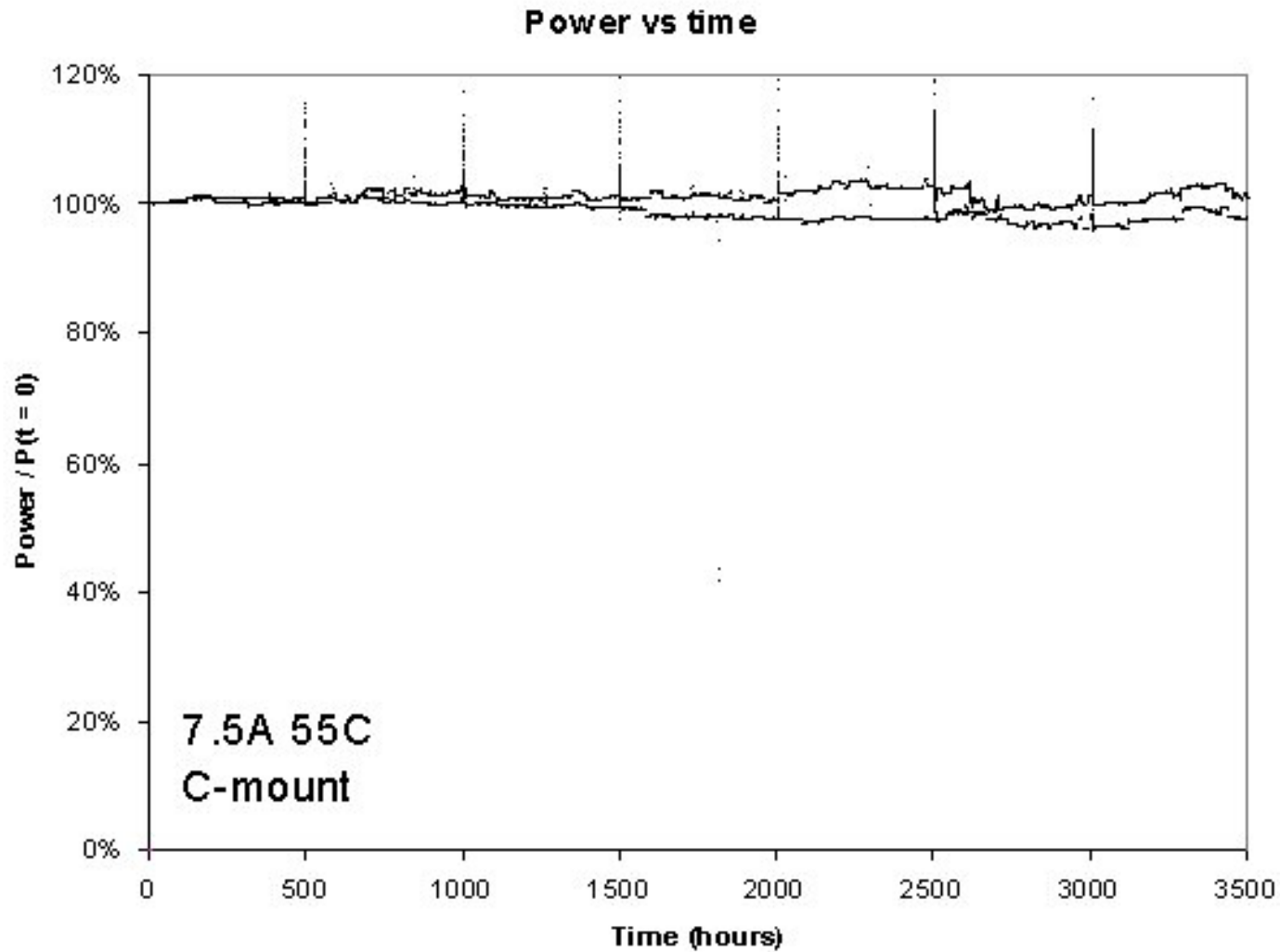
Improve Well, Achieve 76% Peak Efficiency



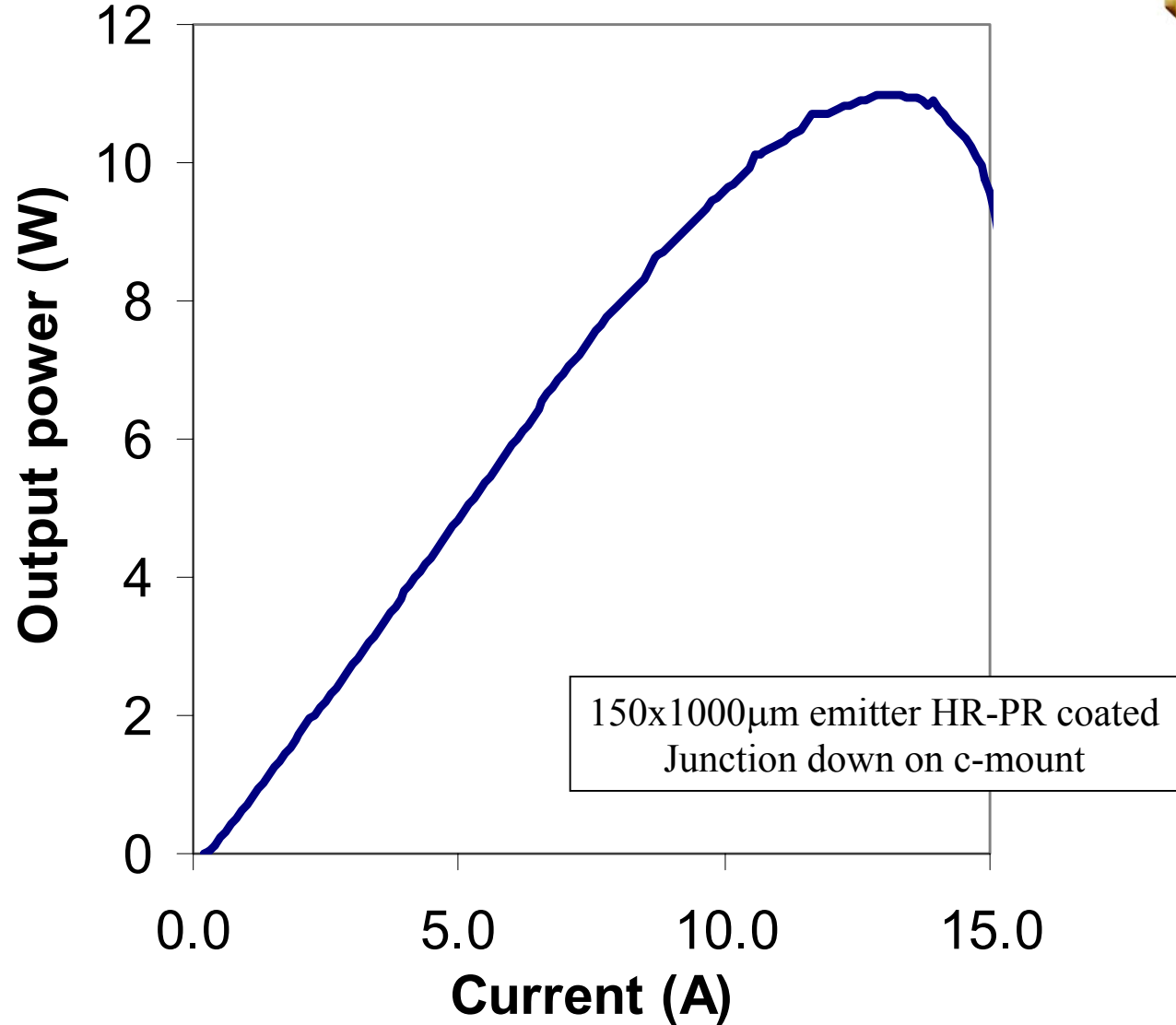
Step Stress Indicates Good Reliability



Long Term Test Confirms Good Reliability



Single Emitter rolls at 10C without damage



- **Thorough Physics-Based Approach Improves laser Efficiency**
 - Delivered rapid progress
- **76% Single Emitter, 73% bar**
 - 84% at 138K
- **Long lifetime, high COMD level**
- **Insights from cryogenic testing helping to drive to > 80%**